

THBT15011, THBT20011, THBT27011

Tripolar overvoltage protection for telecom line

Features

- Bidirectional crowbar protection between tip and gnd, ring and gnd and between tip and ring
- Peak pulse current:
 I_{PP} = 30 A for 10/1000 µs surge
- Holding current: I_H = 150 mA

Complies with Telcordia standards

- Telcordia GR-1089-Core, (second level) with line series resistors
 - 10/1000 µs, 1000 V
 - 2/10 μs, 2500 V (first level)
 - 2/10 µs, 5000 V

Description

Dedicated to telecommunication equipment protection, these devices provide a triple bidirectional protection function.

They ensure the same protection capability with the same breakdown voltage both in longitudinal mode and transversal mode.

A particular attention has been given to the internal wire bonding. The "4-point" configuration ensures a reliable protection, eliminating overvoltages introduced by the parasitic inductances of the wiring (Ldi/dt), especially for very fast transient overvoltages.

Dynamic characteristics have been defined for several types of surges, in order to meet the SLIC maximum ratings.

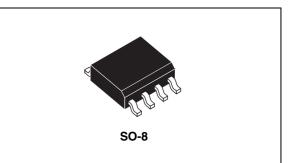
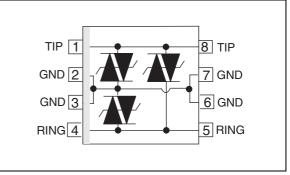


Figure 1. Schematic diagram



1 Characteristics

| Symbol | Parameter | Value | Unit | |
|------------------|---|-----------------------|----------|---|
| I _{PP} | Peak pulse current ⁽¹⁾ | 10 / 1000 µs | 30 | А |
| I _{TSM} | Non repetitive surge peak on-state current (F = 50 Hz) | tp = 10 ms t = 1 s | 8 3.5 | A |
| Tstg Tj | Storage temperature range Maximum junction temperature | - 40 to + 150 150 | °C | |
| TL | Maximum lead temperature for soldering during 10s 260 | | | |

Table 1. Absolute maximum ratings ($T_{amb} = 25 \ ^{\circ}C$)

1. For pulse waveform see Figure 2

Figure 2. Pulse waveform 10/1000 μ s, tr = 10 μ s, tp = 1000 μ s

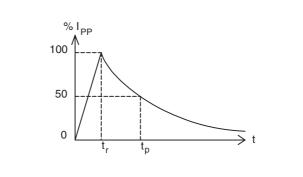


Figure 3. Test circuit for I_{PP}

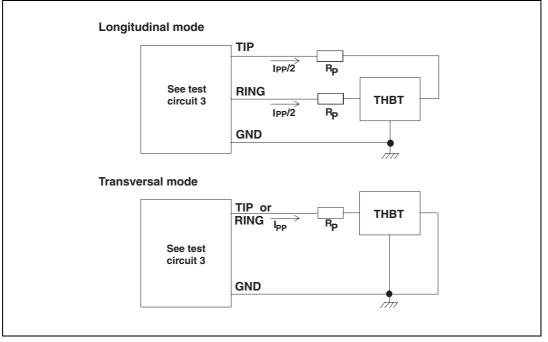




Table 2.Thermal resistance

| Symbol | Parameter | Value | Unit |
|----------------------|---------------------|-------|------|
| R _{th(j-a)} | Junction to ambient | 170 | °C/W |

Table 3.Electrical characteristics (T_{amb} = 25 °C)

| Symbol | Parameter |
|-----------------|--------------------------------------|
| V _{RM} | Stand-off voltage |
| I _{RM} | Leakage current at stand-off voltage |
| V _R | Continuous reverse voltage |
| V _{BR} | Breakdown voltage |
| V _{BO} | Breakover voltage |
| I _Н | Holding current |
| I _{BO} | Breakover current |
| V _F | Forward voltage drop |
| I _{PP} | Peak pulse current |
| С | Capacitance |

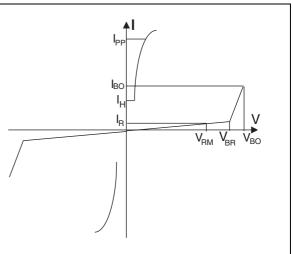


Table 4.Static parameters

| | I _{RM} @ | ₽ V _{RM} | I _R @ | ₽ V _R | ١ | / _{BO} @ I _B | D | Ι _Η | С |
|------------|-------------------|-------------------|------------------------|------------------|-------------|----------------------------------|-----|----------------|-------------|
| Туре | max. | | max. (⁻ | 1) | max. (2) | min. | | min. (3) | max. (4) |
| | μA | V | μA | v | V | V | mA | mA | pF |
| THBT15011D | 5 | 135 | 50 | 150 | 210 | 50 | 400 | 150 | 80 |
| THBT20011D | 5 | 180 | 50 | 200 | 290 | 50 | 400 | 150 | 80 |
| THBT27011D | 5 | 240 | 50 | 270 | 380 | 50 | 400 | 150 | 80 |

1. I_R measured at V_R guarantee V_{BR} min \ge V_R

2. Measured at 50 Hz (1 cycle) - See test circuit 1 (Figure 4).

3. See the reference test circuit 2 (*Figure 5*).

4. $V_R = 1 V \text{ bias}, V_{RMS} = 1 V, F = 1 MHz.$

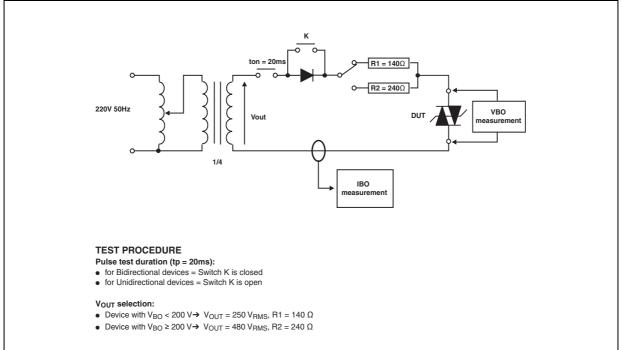


| | • | | • | , | | | |
|------------|-----------------|--------------------------------|-------------------------|----------------------------|--|-------------------|------|
| Туре | Symbol | Test conditions ⁽¹⁾ | | | | | Unit |
| THBT15011D | V _{BO} | 10/700μs 1.2/50μs 2/10μs | 1.5kV 1.5kV 2.5kV | Rp=10Ω Rp=10Ω Rp=62Ω | I _{PP} =30A I _{PP} =30A I _{PP} =38A | 190 190 200 | v |
| THBT20011D | V _{BO} | 10/700μs 1.2/50μs 2/10μs | 1.5kV 1.5kV 2.5kV | Rp=10Ω Rp=10Ω Rp=62Ω | I _{PP} =30A I _{PP} =30A I _{PP} =38A | 270 270 280 | v |
| THBT27011D | V _{BO} | 10/700μs 1.2/50μs 2/10μs | 1.5kV 1.5kV 2.5kV | Rp=10Ω Rp=10Ω Rp=62Ω | I _{PP} =30A I _{PP} =30A I _{PP} =38A | 360 360 400 | v |

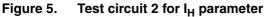
Table 5. Dynamic breakover voltages (transversal mode)

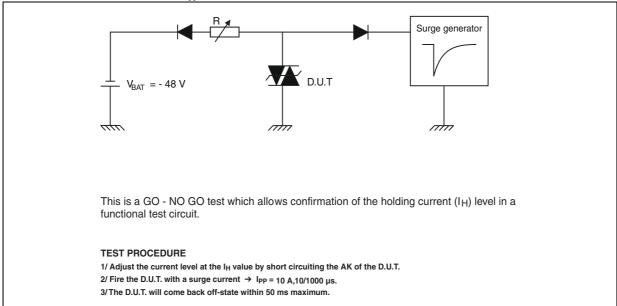
1. See test circuit 3 for V_{BO} dynamic parameters; R_{p} is the protection resistor located on the line card.

Figure 4. Test circuit 1 for I_{BO} and V_{BO} parameters

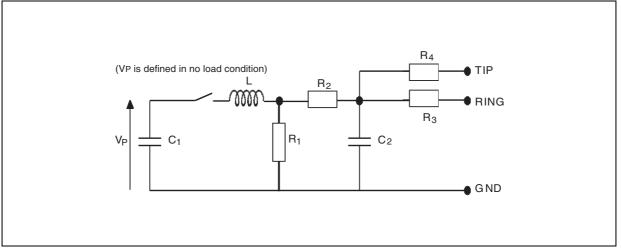






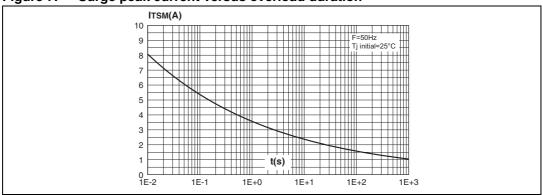






| Pulse | e (µs) | Vp | C ₁ | C ₂ | L | R ₁ | R ₂ | R ₃ | R ₄ | I _{PP} | Rp |
|-------|--------|------|-----------------------|----------------|------|---------------------|---------------------|---------------------|---------------------|-----------------|---------------------|
| tr | tp | (V) | (μF) | (nF) | (µH) | (Ω) | (Ω) | (Ω) | (Ω) | (A) | (Ώ) |
| 10 | 700 | 1500 | 20 | 200 | 0 | 50 | 15 | 25 | 25 | 30 | 10 |
| 1.2 | 50 | 1500 | 1 | 33 | 0 | 76 | 13 | 25 | 25 | 30 | 10 |
| 2 | 10 | 2500 | 10 | 0 | 1.1 | 1.3 | 0 | 3 | 3 | 38 | 62 |





2 Application information

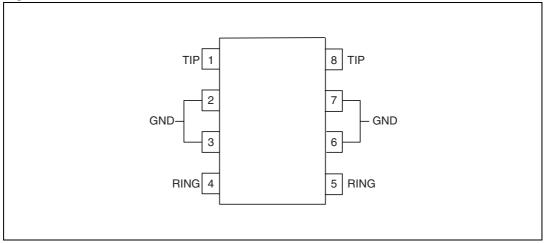


Figure 8. Device connections

1. Connect pins 2, 3, 6 and 7 to Ground to guarantee a good surge current capability for long duration disturbances.

2. To take advantage of the "4-point" structure of the THBT, the TIP and RING lines have to cross the device. In this case, the device will eliminate the overvoltages generated by the parasitic inductances of the wiring (Ldi/dt), especially for very fast transients.

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2.1 Application circuits

Figure 9. Line card protection

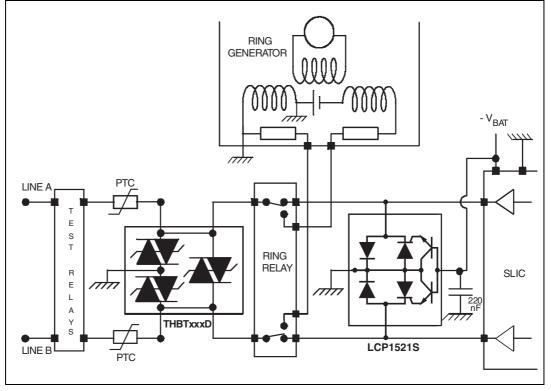
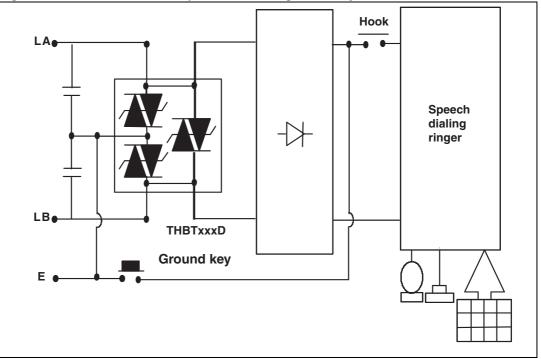


Figure 10. Protection for telephone set with ground key





3 Package information

• Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at *www.st.com*.

Table 7. SO-8 dimensions

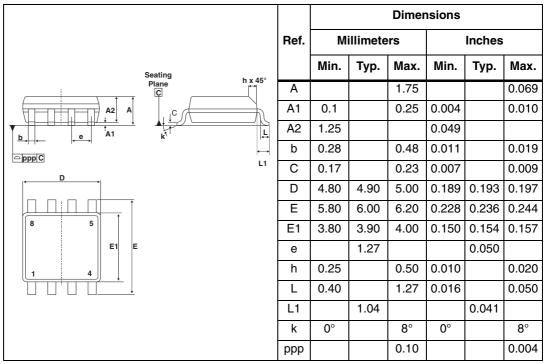
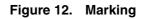
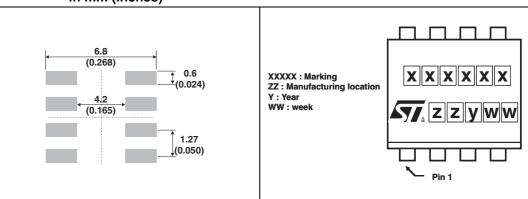


Figure 11. Footprint dimensions in mm (inches)







4 Ordering information scheme

Figure 13. Ordering information scheme

| Bidirectional Trisil Breakdown voltage | THBT 150 1 1 D RL |
|---|-------------------|
| Version Package | |
| 1 = SO-8 plastic Dynamic Packing | |
| (Blank) = Tube RL = Tape and reel | |

5 Ordering information

Table 8.Ordering information

| Order code | Marking | Package | Weight |
|------------|---------|---------|---------|
| THBT15011D | BT151D | | |
| THBT20011D | BT201D | SO-8 | 0.077 g |
| THBT27011D | BT271D | | |

6 Revision history

Table 9. Document revision history

| Date | Revision | Changes |
|-------------|----------|---|
| Oct-2003 | 7A | Previous release |
| 19-Feb-2008 | 8 | Reformatted to current standards. Removed THBT16011D from <i>Table 4</i> and <i>Table 8</i> . Updated <i>Figure 4</i> , <i>Figure 5</i> , and <i>Figure 9</i> . Added ECOPACK paragraph in <i>Section 3</i> . Added <i>Figure 12: Marking</i> . |



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